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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 08/996,325
Priority Filing Date December 22, 1997
Inventor H. Montgomery Manning
Assignee Micron Technology, Inc.
Priority Group Art Unit 2822
Priority Examiner M. Trinh
Attorney's Docket No. MI22-1698
Title: Methods of Forming Thin Film Transistors

INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.


The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending Application Serial No. 08/996,325, filed December 22, 1997, upon which the above-identified application relies for a priority dated under 35 U.S.C. §120. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2). As a courtesy, Applicant submits copies of the cited references for review.

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Citation of these references is respectfully requested.

Respectfully submitted,

Date: Aug 1, 2001



Bernard Berman

Reg. No. 37,279

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Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
M122-1693SERIAL NO.
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GROUP

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	4,243,997	01/06/81	Natori et al.			
	AB	4,467,518	03/28/84	Bansal et al.			
	AC	4,845,537	07/04/89	Nishimura et al.			
	AD	4,864,374	09/05/89	Banerjee			
	AE	5,001,540	03/19/91	Ishihara			
	AF	5,156,987	10/20/92	Sandhu et al.			
	AG	5,208,172	05/04/93	Fitch et al.			
	AH	5,214,295	05/25/93	Manning			
	AI	5,229,310	07/20/93	Sivan			
	AJ	5,270,968	12/14/93	Kim et al.			
	AK	5,274,259	12/28/93	Grabowski et al.			

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

	AR		"A New Toroidal TFT Structure for Future Generation SRAMs"; J. D. Hayden et al.; 1993 IEEE; pp. 33.5.1-33.5.4
	AS		"Methods of Forming Small Contact Holes"; IBM Technical Disclosure Bulletin; January 1983; Vol 30 No. 3; pp. 233-235
	AT		

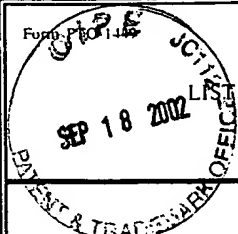
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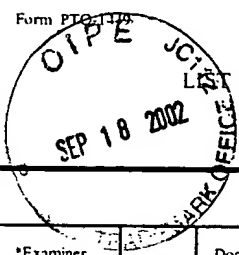
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U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,283,455	02/01/94	Inoue et al.			
	AB	5,334,862	08/02/94	Manning et al.			
	AC	5,397,731	03/14/95	Takemura			
	AD	5,418,393	05/23/95	Hayden			
	AE	5,432,370	07/11/95	Kitamura et al.			
	AF	5,444,275	08/22/95	Kugishima et al.			
	AG	5,463,240	10/31/95	Watanabe			
	AH	5,508,531	04/16/96	Ha			
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	AJ	5,599,724	02/04/97	Yoshida			
	AK	5,612,546	03/18/97	Choi et al.			
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		Document Number	Date	Country	Class	Subclass	Translation
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U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,640,034	06/17/97	Malhi			
	AB	5,700,727	12/23/97	Manning			
	AC	5,747,359	05/05/98	Yuan et al.			
	AD	5,925,894	07/20/99	Yang			
	AE	5,994,735	11/30/99	Maeda et al.			
	AF	6,175,134	01/16/01	Manning			
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FOREIGN PATENT DOCUMENTS							
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